EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5209	content adj addressable adj memory	US-PGPUB; USPAT	OR	ON	2006/02/23 17:10
L2	244127	cam	US-PGPUB; USPAT	OR	ON	2006/02/23 17:10
L3	245319	1 or 2	US-PGPUB; USPAT	OR	ON	2006/02/23 17:10
L4	5742	feram or fram	US-PGPUB; USPAT	OR	ON	2006/02/23 17:11
L5	189	3 and 4	US-PGPUB; USPAT	OR	ON	2006/02/23 17:11
L6	2905	table adj pointer	US-PGPUB; USPAT	OR	ON	2006/02/23 17:11
L7	1	5 and 6	US-PGPUB; USPAT	OR	ON	2006/02/23 17:12
L8	10	mcadams-hugh.in.	US-PGPUB; USPAT	OR	ON	2006/02/23 17:12

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = MCADAMS
First Name = HUGH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08756094	5790467	150	11/25/1996	APPARATUS AND METHOD FOR A DIRECT-SENSE SENSE AMPLIFIER WITH A SINGLE READ/WRITE CONTROL LINE	MCADAMS, HUGH
08756314	5831919	150	11/25/1996	APPARATUS AND METHOD FOR A DIRECT-SENSE SENSE-AMPLIFIER WITH DECODED READ AND WRITE Y-SELECT	MCADAMS, HUGH
08782225	Not Issued	168	01/13/1997	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A CONTROLLED OVERDRIVING CIRUCIT	MCADAMS, HUGH
08847350	5859807	150	04/24/1997	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A CONTROLLED OVERDRIVING CIRCUIT	MCADAMS, HUGH
10185206	6721217	150	06/27/2002	METHODS AND APPARATUS FOR MEMORY SENSING	MCADAMS, HUGH
10454862	6819601	150	06/05/2003	PROGRAMMABLE REFERENCE FOR 1T/1C FERROELECTRIC MEMORIES	MCADAMS, HUGH
10614299	6809954	150	07/02/2003	CIRCUIT AND METHOD FOR REDUCING ACCESS TRANSISTOR GATE OXIDE STRESS	MCADAMS, HUGH
10644239	Not Issued	83	08/19/2003	Circuit and method for reducing fatigue in ferroelectric memories	MCADAMS, HUGH
60007675	Not Issued	159	11/29/1995	APPARATUS AND METHOD FOR A DIRECT-SENSE	MCADAMS, HUGH

				AMPLIFIER WITH DECODED READ AND WRITE Y-SELECT	
60009984	Not Issued	159	01/26/1996	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A CONTROLLED OVERDRIVING CIRCUIT	MCADAMS, HUGH
60452658	Not Issued	159	03/07/2003	Programmable reference for 1T/1C ferroelectric memories	MCADAMS, HUGH
60021524	Not Issued	159	07/11/1996	DATA SEQUENCING ANDREGISTERING IN A FOUR BIT PRE-FETCH SDRAM	MCADAMS, HUGH P
09991571	6563753	150	11/16/2001	SENSE AMPLIFIER WITH INDEPENDENT WRITE-BACK CAPABILITY FOR FERROELECTRIC RANDOM-ACCESS MEMORIES	MCADAMS, HUGH P.
10115753	6704218	150	04/02/2002	FERAM WITH A SINGLE ACCESS/ MULTIPLE-COMPARISON OPERATION	MCADAMS, HUGH P.
10154647	6667896	150	05/24/2002	GROUPED PLATE LINE DRIVE ARCHITECTURE AND METHOD	MCADAMS, HUGH P.
10158279	6590799	150	05/29/2002	ON-CHIP CHARGE DISTRIBUTION MEASUREMENT CIRCUIT	MCADAMS, HUGH P.
10190078	6785629	150	07/02/2002	ACCURACY DETERMINATION IN BIT LINE VOLTAGE MEASUREMENTS	MCADAMS, HUGH P.
10190102	6735106	150	07/02/2002	ACCELERATED FATIGUE TESTING	MCADAMS, HUGH P.
10190370	6714469	150	07/02/2002	ON-CHIP COMPRESSION OF CHARGE DISTRIBUTION DATA	MCADAMS, HUGH P.
10190408	6952623	150	07/02/2002	PERMANENT CHIP ID USING FERAM	MCADAMS, HUGH P.
10245081	6757206	150	09/17/2002	SENSE AMPLIFIER WITH OVERRIDE WRITE CIRCUITRY	MCADAMS, HUGH P.
10442360	6804141	150	05/20/2003	DYNAMIC REFERENCE VOLTAGE CALIBRATION INTEGRATED IN FERAMS	MCADAMS, HUGH P.
10805809	Not Issued	41	03/22/2004	Method and apparatus to reduce storage node disturbance in	MCADAMS, HUGH P.

				ferroelectric memory	
10847412	6970371	150	05/17/2004	REFERENCE GENERATOR SYSTEM AND METHODS FOR READING FERROELECTRIC MEMORY CELLS USING REDUCED BITLINE VOLTAGES	MCADAMS, HUGH P.
11200390	Not Issued	30	08/09/2005	High granularity redundancy for ferroelectric memories	MCADAMS, HUGH P.
07499131	5117426	150	03/26/1990	CIRCUIT, DEVICE, AND METHOD TO DETECT VOLTAGE LEAKAGE	MCADAMS, HUGH P.
07560542	Not Issued	166	07/31/1990	VOLTAGE LEVEL DETECTION CIRCUIT	MCADAMS, HUGH P.
07560935	Not Issued	166	07/31/1990	METHOD FOR INITIALIZING REDUNDANT CIRCUITRY	MCADAMS, HUGH P.
07560983	5131018	150	07/31/1990	COUNTER CIRCUIT WITH TWO TRI-STATE LATCHES	MCADAMS, HUGH P.
07626638	Not Issued	161	12/07/1990	BRUSHLESS D.C. MOTOR CONTROLLER USING INTERLOCK CIRCUITS	MCADAMS, HUGH P.
07752111	5303180	150	08/29/1991	PIN PROGRAMMABLE DRAM THAT ALLOWS CUSTOMER TO PROGRAM OPTION DESIRED	MCADAMS, HUGH P.
07805355	Not Issued	166	12/09/1991	PIN PROGRAMMABLE DRAM THAT PROVIDES CUSTOMER OPTION PROGRAMMABILITY	MCADAMS, HUGH P.
07840418	5301160	150	02/24/1992	COMPUTER INCLUDING AN INTEGRATED CIRCUIT HAVING A LOW POWER SELECTION CONTROL ARRANGEMENT	MCADAMS, HUGH P.
07840419	5274828	150	02/24/1992	COMPUTER INCLUDING AN INTEGRATED CIRCUIT HAVING AN ON-CHIP HIGH VOLTAGE SOURCE	MCADAMS, HUGH P.
07892390	5297086	150	05/27/1992	METHOD FOR INITIALIZING REDUNDANT CIRCUITRY	MCADAMS, HUGH P.
07975494	Not Issued	166	11/10/1992	HIGH EFFICIENCY SEMICONDUCTOR SUBSTRATE BIAS PUMP	MCADAMS, HUGH P.
08083427	Not Issued	166	06/28/1993	LOW POWER SUBSTRATE BIAS CIRCUIT	MCADAMS, HUGH P.

08119794	Not Issued	166	09/10/1993	A VERY LARGE SCALE INTEGRATED CIRCUIT MEMORY DEVICE HAVING A TWO STAGE REDUNDANCY DECODING CIRCUIT	MCADAMS, HUGH P.
08154371	5353250	150	11/16/1993	PIN PROGRAMMABLE DRAM THAT PROVIDES CUSTOMER OPTION PROGRAMMABILITY	MCADAMS, HUGH P.
08210789	Not Issued	163	03/18/1994	LOW POWER REDUNDANT FUSE CIRCUIT	MCADAMS, HUGH P.
08258640	5642321	150	06/10/1994	VOLTAGE LEVEL DETECTION CIRCUIT	MCADAMS, HUGH P.
08288131	6031411	150	08/09/1994	LOW POWER SUBSTRATE BIAS CIRCUIT	MCADAMS, HUGH P.
08298827	5502671	150	08/31/1994	APPARATUS AND METHOD FOR A SEMICONDUCTOR MEMORY CONFIGURATION-DEPENDENT OUTPUT BUFFER SUPPLY CIRCUIT	MCADAMS, HUGH P.
08320615	5553033	150	10/07/1994	APPARATUS AND METHOD FOR AN ADDRESS TRANSITION DETECTOR SUMMING CIRCUIT	MCADAMS, HUGH P.
08343276	6137342	150	11/22/1994	HIGH EFFICIENCY SEMICONDUCTOR SUBSTRATE BIAS PUMP	MCADAMS, HUGH P.
08346728	5519666	150	11/30/1994	APPARATUS AND METHOD FOR AN ADDRESS TRANSITION DETECTOR	MCADAMS, HUGH P.
08435689	5696721	150	05/05/1995	DYNAMIC RANDOM ACCESS MEMORY HAVING ROW DECODER WITH LEVEL TRANSLATOR FOR DRIVING A WORD LINE VOLTAGE ABOVE AND BELOW AN OPERATING SUPPLY VOLTAGE RANGE	MCADAMS, HUGH P.
08474240	Not Issued	161	06/07/1995	CIRCUIT AND METHOD FOR TWO STAGE REDUNDANCY DECODING	MCADAMS, HUGH P.
08479023	Not Issued	166	06/07/1995	CIRCUIT AND METHOD FOR TWO STAGE REDUNDANCY DECODING	MCADAMS, HUGH P.
08482706	Not Issued	161	06/07/1995	A CIRCUIT AND METHOD FOR TWO STAGE	MCADAMS, HUGH P.

| | | | | REDUNDANCY DECODING | | |

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Your Search was:

Last Name = MCADAMS
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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08484036	Not Issued	161	06/07/1995	VERY LARGE SCALE INTEGRATED CIRCUIT MEMORY DEVICE HAVING A TWO STAGE REDUNDANCY DECODING CIRCUIT	MCADAMS, HUGH P.
08485597	6239650	150	06/07/1995	LOW POWER SUBSTRATE BIAS CIRCUIT	MCADAMS, HUGH P.
08717540	5802005	150	09/23/1996	FOUR BIT PRE-FETCH SDRAM COLUMN SELECT ARCHITECTURE	MCADAMS, HUGH P.
08839004	5792682	150	04/23/1997	METHOD FOR REDUCING CHARGE LOSS	MCADAMS, HUGH P.
08883738	5841688	150	06/27/1997	MATCHED DELAY WORD LINE STRAP	MCADAMS, HUGH P.
08883973	6100588	150	06/27/1997	MULTIPLE LEVEL CONDUCTOR WORDLINE STRAPPING SCHEME	MCADAMS, HUGH P.
08891596	5953278	150	07/11/1997	DATA SEQUENCING AND REGISTERING IN A FOUR BIT PRE-FETCH SDRAM	MCADAMS, HUGH P.
08905776	Not Issued	161	07/01/1997	CIRCUIT AND METHOD FOR TWO STAGE REDUNDANCY DECODING	MCADAMS, HUGH P.
09027029	5978254	150	02/20/1998	SEMICONDUCTOR MEMORY STRUCTURE FOR IMPROVED CHARGE STORAGE	MCADAMS, HUGH P.
09330469	6275084	150	06/10/1999	PHASE MODULATED INPUT/ OUTPUT CIRCUIT	MCADAMS, HUGH P.
60007615	Not Issued	159	11/29/1995	APPARATUS AND METHOD FOR A DIRECT-SENSE SENSE AMPLIFIER WITH A SINGLE READ/WRITE CONTROL LINE	MCADAMS, HUGH P.

60020674	Not Issued	159	06/28/1996	MATCHED DELAY WORD LINE STRAP	MCADAMS, HUGH P.
60020961	Not Issued	159	06/28/1996	MULTIPLE LEVEL CONDUCTOR WORDLINE STRAPPING SCHEME	MCADAMS, HUGH P.
60089558	Not Issued	159	06/17/1998	PHASE MODULATED I/O	MCADAMS, HUGH P.
10150744	6807080	150	05/17/2002	ENHANCED STORAGE STATES IN AN MEMORY	MCADAMS, HUGH PRYOR

Inventor Search Completed: No Records to Display.

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